

(Use several sheets if necessary)

13692

**09/870,841**

**Applicant(s)**

**Richard S. NORMAN et al.**

**Filing Date**

**June 1, 2001**

### Group Art Unit

2661

[illegible]

REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO
See 1.	WO 98/26539	06-18-1998	International				
See 2.	EP 1 051 001 A2	11-08-2000	Europe				

file 1.	Partial International Search; PCT/CA02/00810

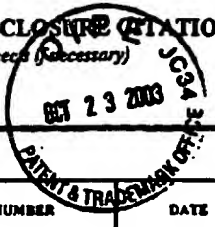
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**INFORMATION DISCLOSURE CITATION**  
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**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
Allee	1	U.S. 5,790,539	08-04-1998	Chao et al.			
Allee	2	U.S. 5,831,980	11-03-1998	Varma et al.			
Allee	3	U.S. 6,069,895	05-30-2000	Ayandeh			

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**FOREIGN PATENT DOCUMENTS**

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
Allee	4	EP 0 241 152 A2	10-14-1987	EUROPE				

**OTHER DOCUMENTS** (Including Author, Title, Date, Pertinent Pages, Etc.)

Allee	5	NOTANI H ET AL: "An 8*8 ATM switch LSI with shared multi-buffer architecture" PROCEEDINGS OF THE SYMPOSIUM ON VLSI CIRCUITS. SEATTLE, JUNE 4-6, 1992, SYMPOSIUM ON VLSI CIRCUITS, NEW YORK, IEEE, US, 4 June 1992 (1992-06-04), pages 74-75, XP010064987					
Allee	6	International Search Report PCT/CA02/00810, August 1, 2003					

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**INFORMATION DISCLOSURE CITATION**  
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84/870841  
08/01/01

**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE - IF APPROPRIATE
Alee	1.	U.S. 5,008,878	16/04/1991	Ahmadi et al.			
Alee	2.	U.S. Re. 34,755	11/10/1994	Eng et al.			
Alee	3.	U.S. Re. 34,811	27/12/1994	Eng et al.			
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							YES	NO
Alee	12.	EP 0 680 173 A1	02/11/1995	EUROPE				

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Alee	13.	Nick McKeown et al. "The Tiny Tera: a Packet Switch Core"; Departments of Electrical Engineering and Computer Science, Stanford University; August 1996; <a href="http://tiny-tera.stanford.edu/tiny-tera/index.html">http://tiny-tera.stanford.edu/tiny-tera/index.html</a>

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<b>Applicant(s)</b> <b>Richard S. Norman et al.</b>	
<b>Filing Date</b> <b>06/01/2001</b>	<b>Group Art Unit</b>

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Andrew

08 NOV 2004

**SHEET 1 OF 1**



**Notice of References Cited**

Application/Control No.

09/870,841

Applicant(s)/Patent Under  
Reexamination  
NORMAN ET AL.

Examiner

Andrew C Lee

Art Unit

2664

Page 1 of 1

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	B	US-6,804,731 B1	10-2004	Chang et al.	710/53
	C	US-6,754,216 B1	06-2004	Wong et al.	370/395.42
	D	US-6,424,659 B2	07-2002	Viswanadham et al.	370/469
	E	US-6,735,173 B1	05-2004	Lenoski et al.	370/235
	F	US-6,134,217	10-2000	Stiliadis et al.	370/232
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	H	US-6,751,562 B1	06-2004	Blackett et al.	702/61
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	J	US-			
	K	US-			
	L	US-			
	M	US-			

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	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

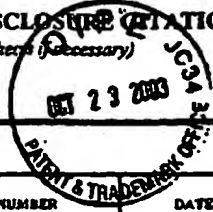
*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.



**INFORMATION DISCLOSURE CITATION**  
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13692

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Applicant(s)  
Richard S. NORMAN et al.

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EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
See	1	U.S. 5,790,539	08-04-1998	Chao et al.			
See	2	U.S. 5,831,980	11-03-1998	Varma et al.			
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See	6	International Search Report PCT/CA02/00810, August 1, 2003

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EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
Hell <sup>1.</sup>		U.S. 4,849,751	18/07/1989	Barber et al.			
Hell <sup>2.</sup>		U.S. 5,072,366	10/12/1991	Simcoe			
Hell <sup>3.</sup>		U.S. 4,955,020	04/09/1990	Stone et al.			

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Free	2	Joseph Desposito; Router-On-A-Chip Manages Network Traffic with Wire-Speed QoS; Electronic Design; May 1, 2000; pp 64-65-66;

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